

FIG. 1

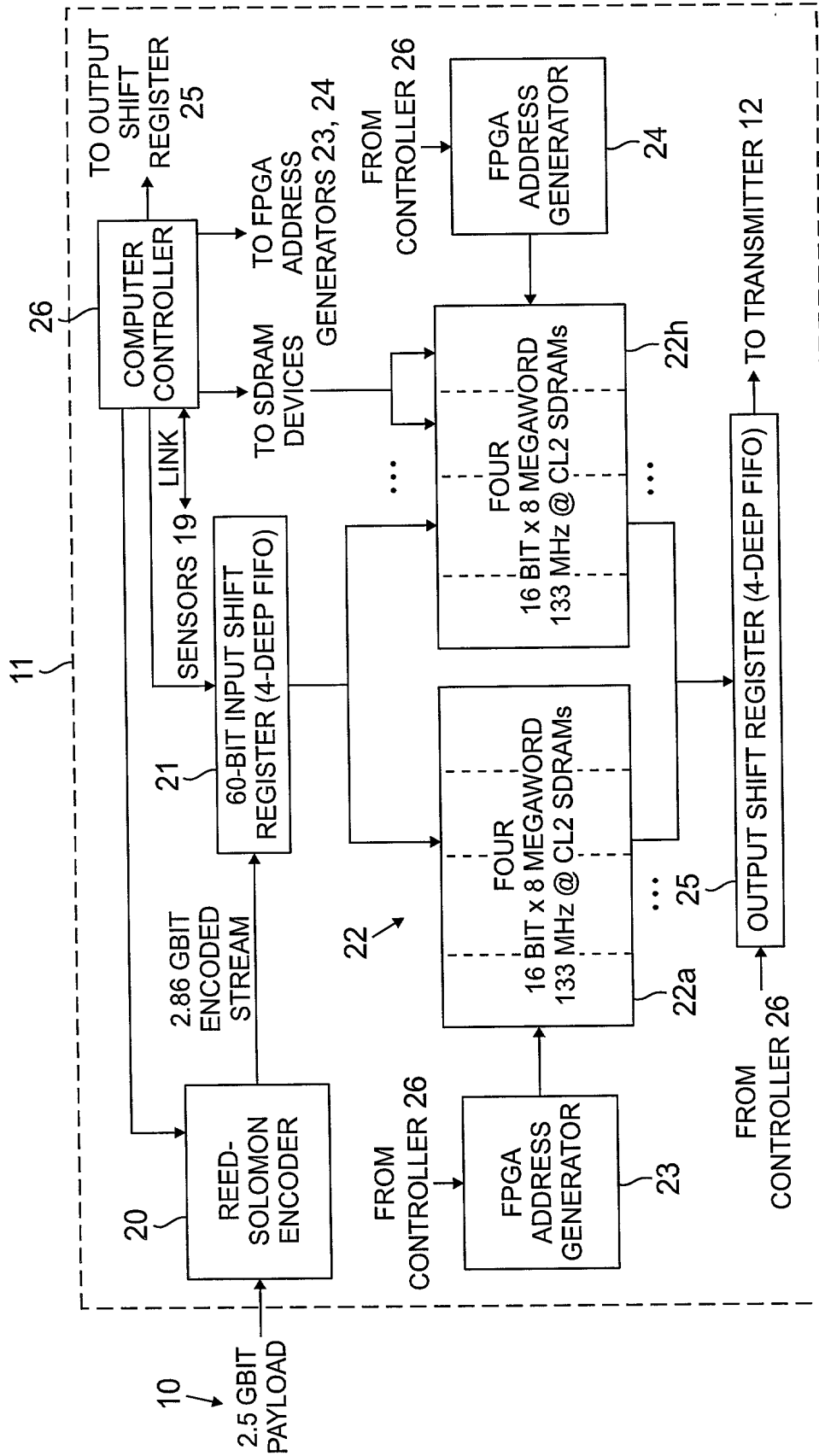
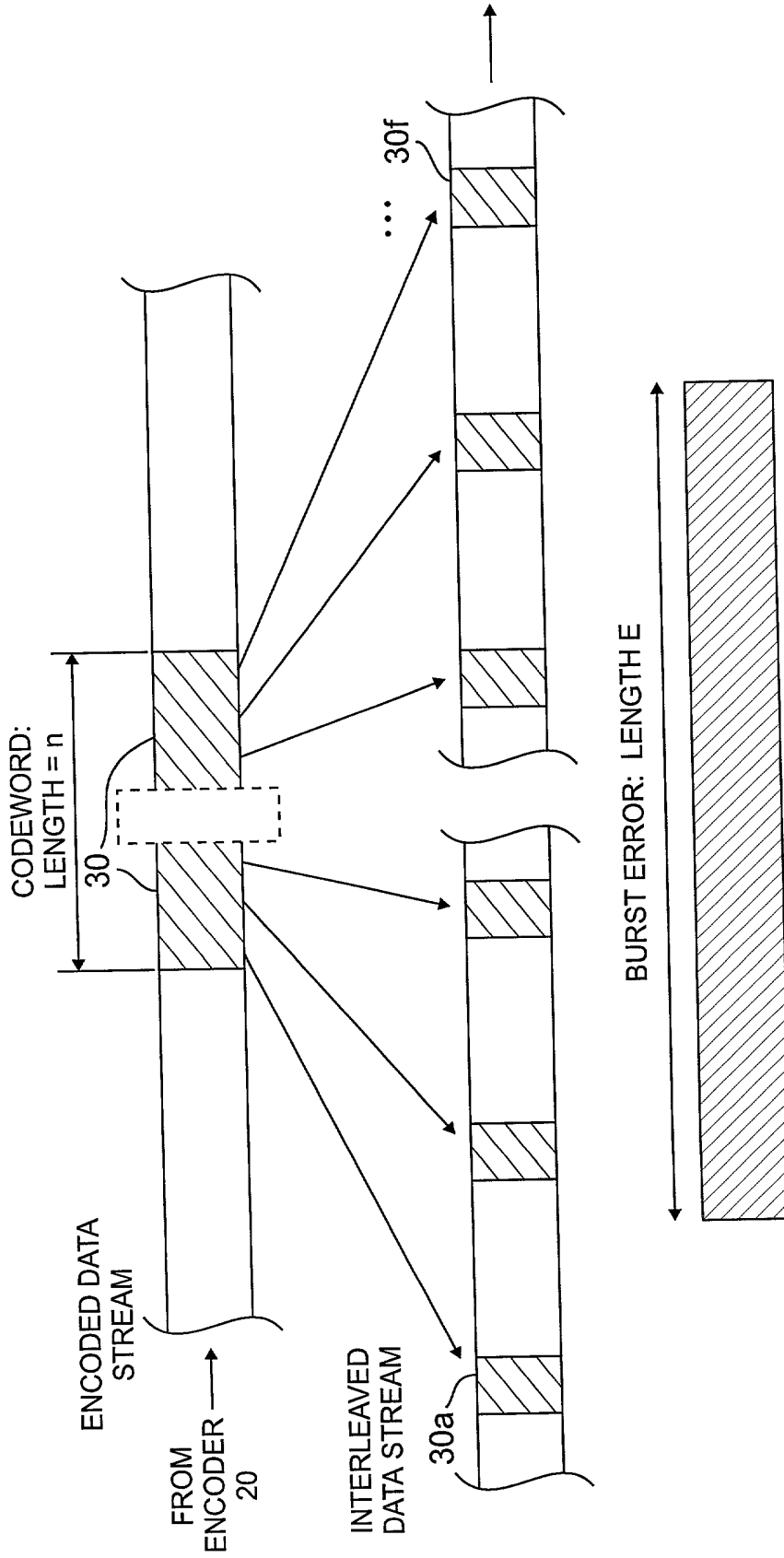


FIG. 2



INTERLEAVING THE ENCODED PAYLOAD

FIG. 3

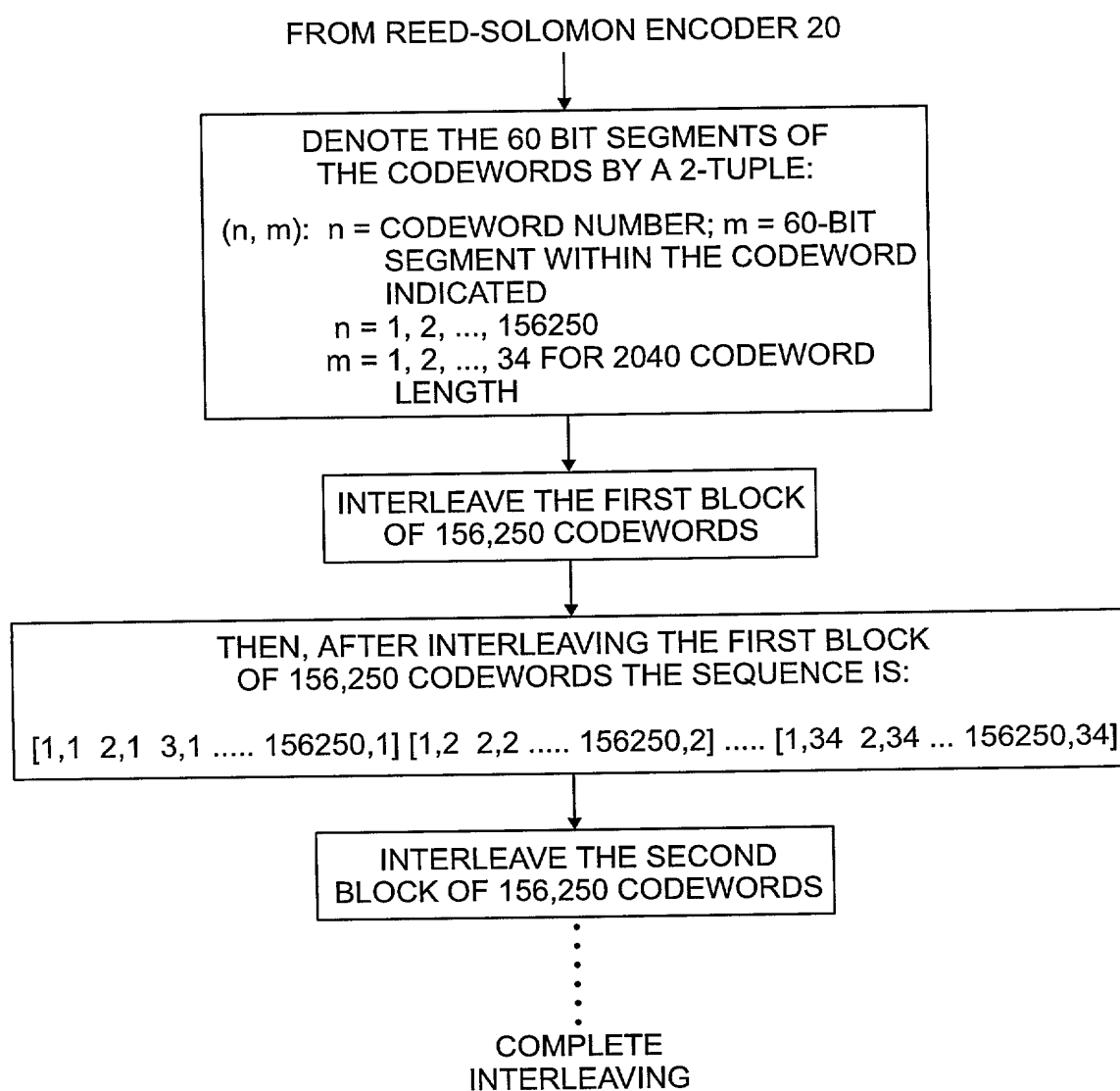


FIG. 4

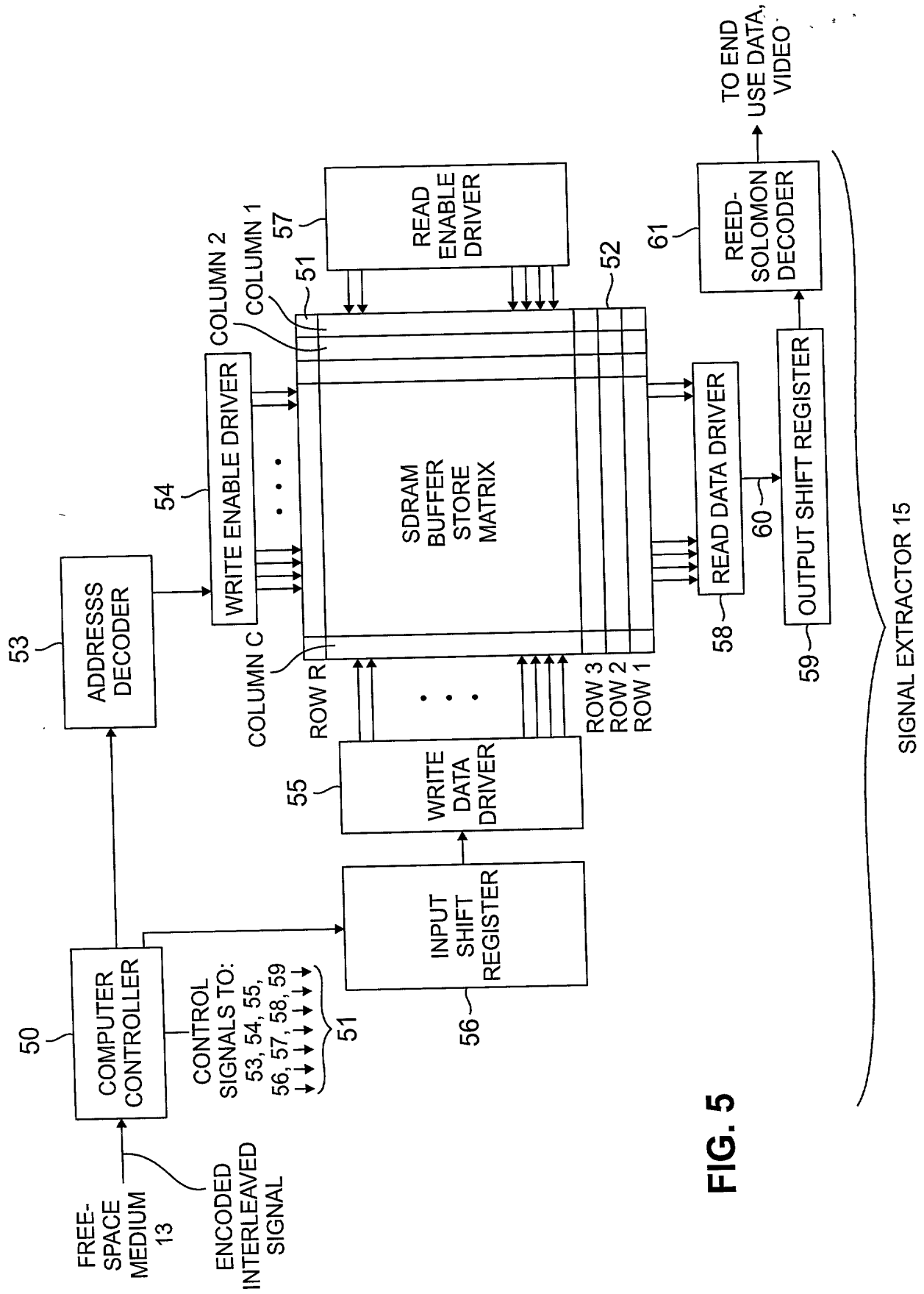


FIG. 5

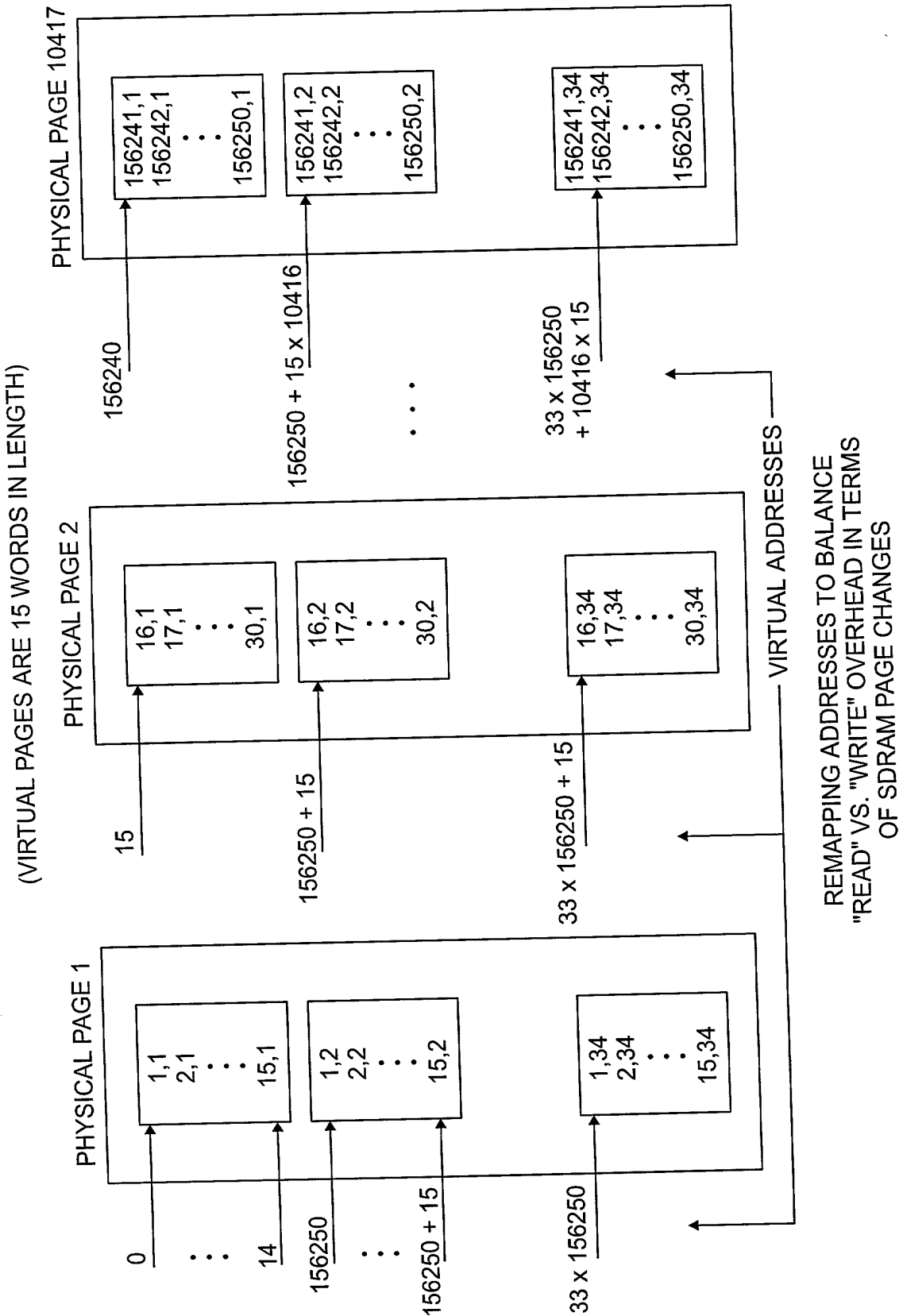


FIG. 6A

REMAPPING ADDRESSES TO BALANCE
"READ" VS. "WRITE" OVERHEAD IN TERMS
OF SDRAM PAGE CHANGES

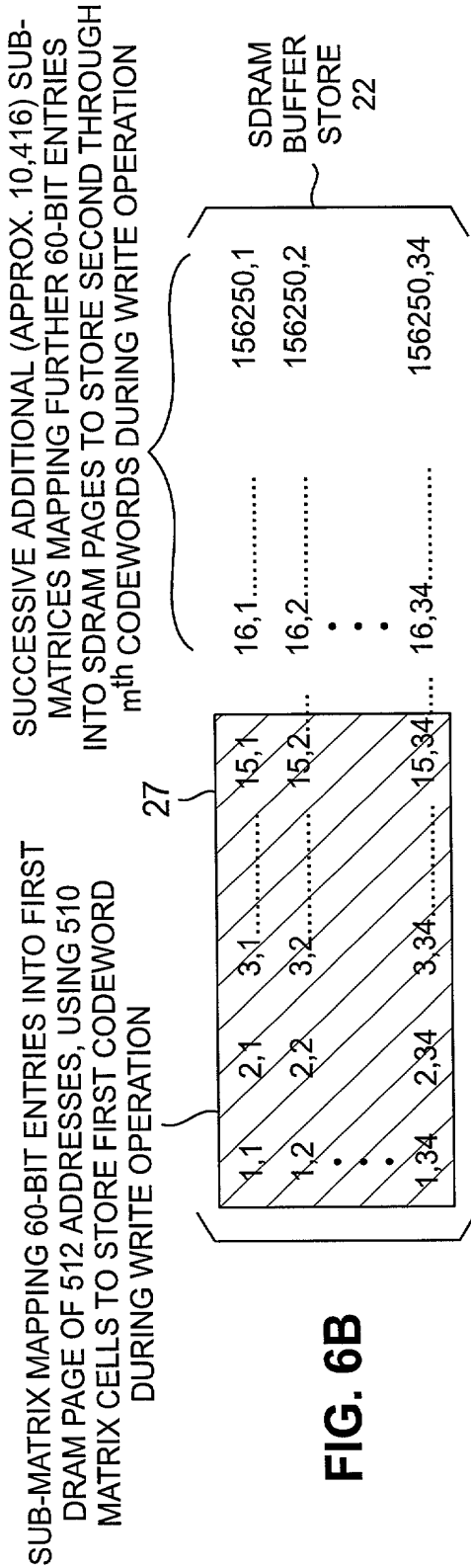


FIG. 6B

CODEWORD SEGMENTS EXPRESSED AS A MATRIX

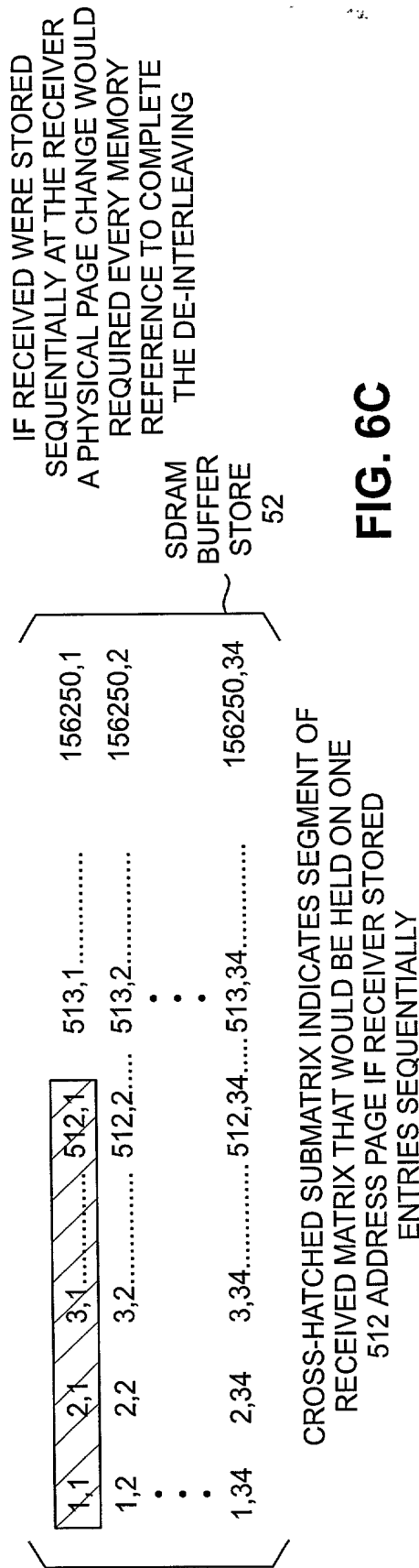


FIG. 6C

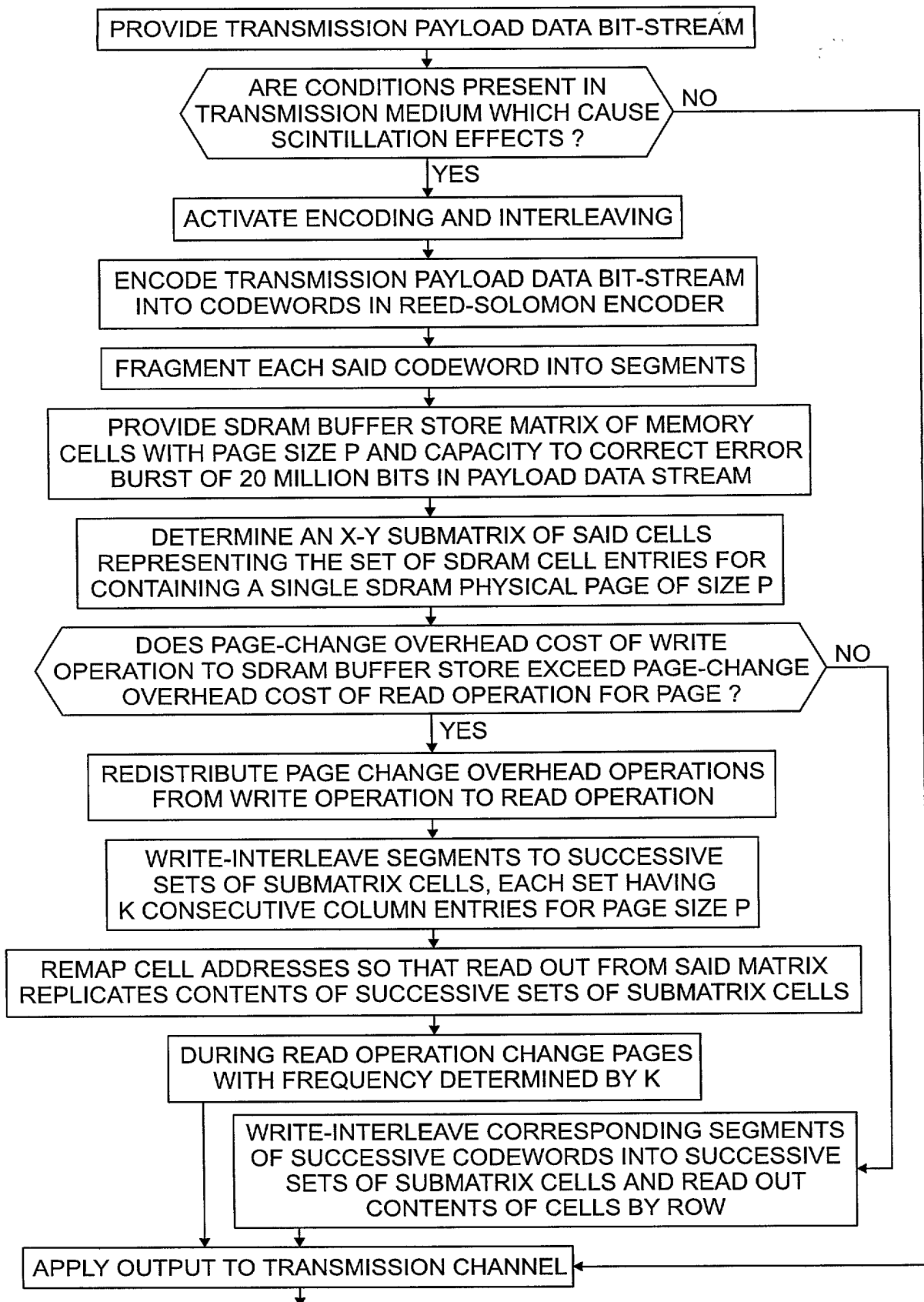
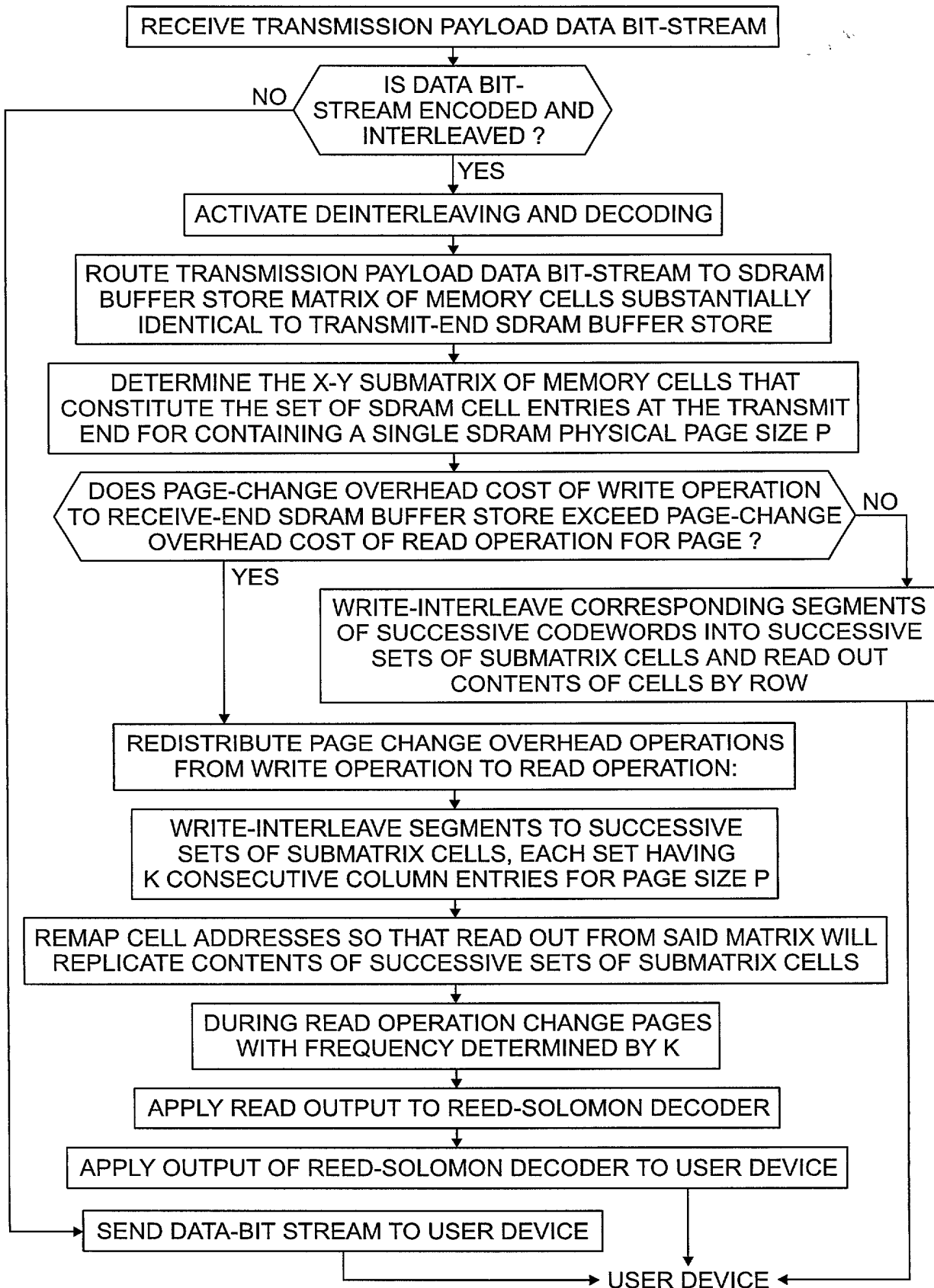


FIG. 7A

FLOW CHART OF PROCESS
AT TRANSMITTER END



FLOW CHART OF PROCESS
AT RECEIVER END

FIG. 7B